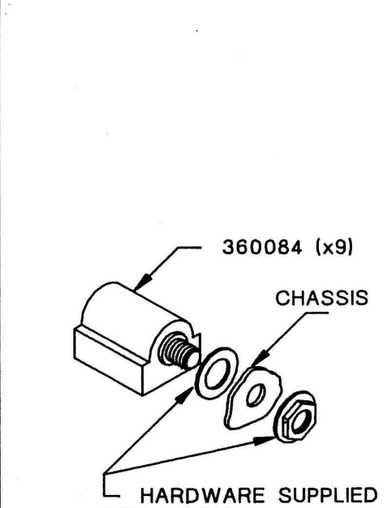
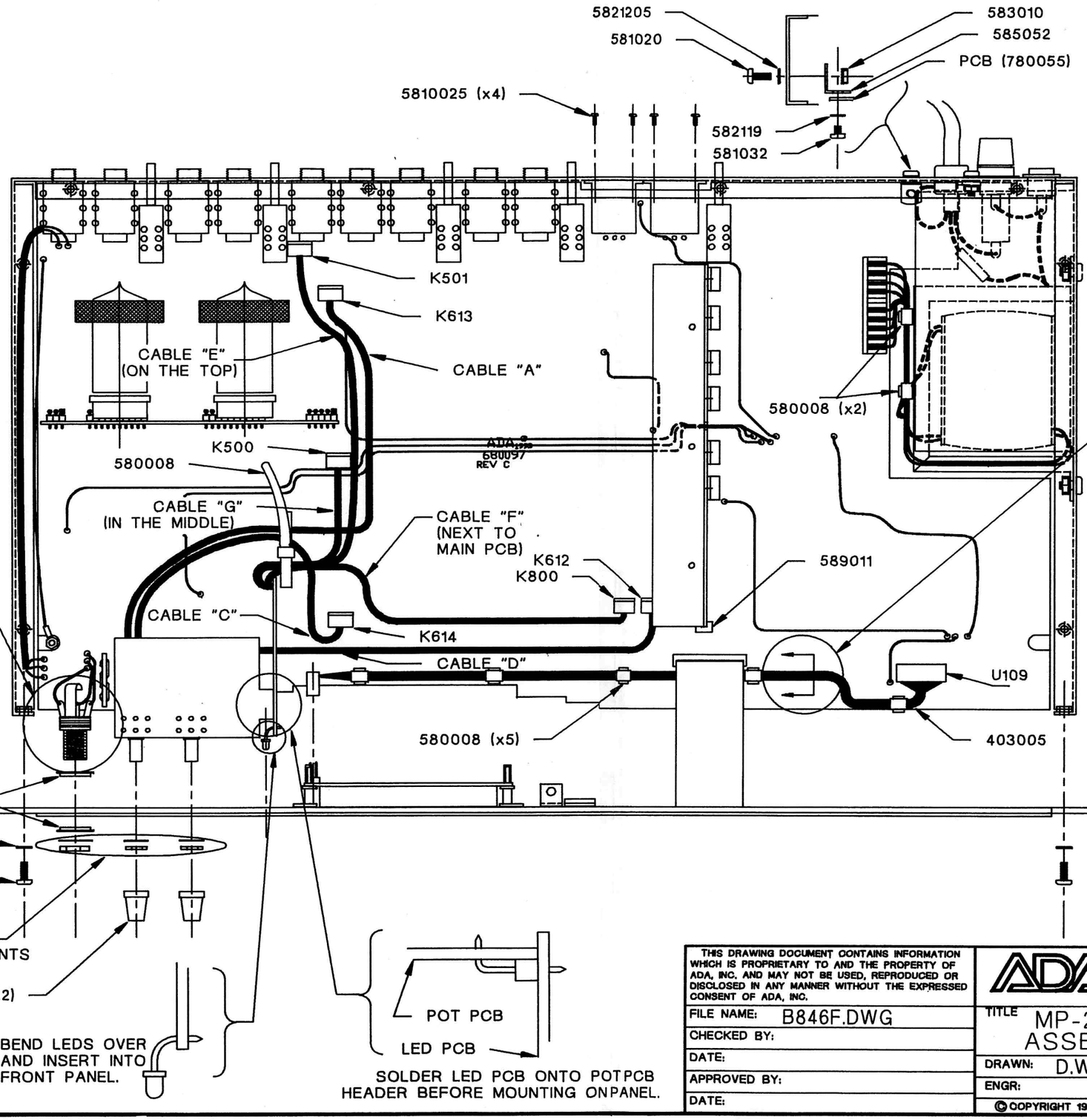
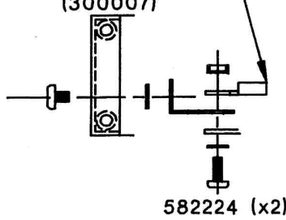
REVISION HISTORY				
ZONE	REV.	DESCRIPTION	SIGNATURE	DATE
-	A	ADD FISH PAPER; RMVD PC GND		8/4/93
-	B	REV C PC CHGS; NEW DWG		10/12/93
-	C	CHG RIBBON FOLD; ADDED TIES		10/18/93
D3	D	CHG'D No. 8 WASHERS		11/2/93
-	E	ADDED TIE WRAPS AT XFMR		11/9/93
-	F	ADDED GROMMET STRIP		11/17/93
-	-	-		-

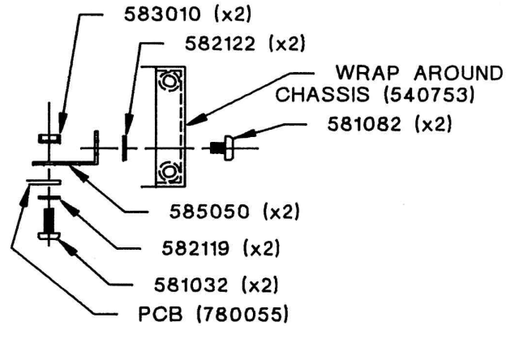




LCD RIBBON CABLE IS FOLDED TIGHTLY WITH CABLE TIES AT THE ENDS AND IN BETWEEN.

ROTATE AND BEND JACK LUGS SO THAT NONE SHORT TO THE PCB OR SHIELD GROUND PLANE.





SOLDER LED PCB ONTO POT PCB HEADER BEFORE MOUNTING ON PANEL.

DEPOT http://www.adadepot.com

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FILE NAME: B846F.DWG
CHECKED BY:
DATE:
APPROVED BY:
DATE:

SIGNAL PROCESSORS, INC.
420 LESSER STREET, OAKLAND, CA 94601

TITLE: MP-2, FINAL CHASSIS ASSEMBLY

DRAWN: D.WIRT
ENGR:

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SCALE:
SIZE: B
DRAWING NO.: 846
REV: F
DATE: 10/12/93
SHEET 1 OF 1