XLR-PINOUT
PIN1 = SHIELD
PIN2 = a, +
PIN3 = b, -

✳ = INPUT TRANSFORMER OPTIONAL

INPUT A
INPUT B
OUTPUT A
OUTPUT B
0dBu
B1 B2 B3 B4
JR1 JR2 JR3 JR4 0
T1 T2 DCN308840
H1A H1B H1C H1D 10k
H3A H3B H3C H3D 10k
C315 C317 33p
C316 C318 33p
R300 R316 10
R301 R317 10
C300 C306 1µ0
C301 C307 1µ0
R302 R319 5k6
R303 R318 11k
R304 R320 620
R305 R321 510
C302 C308 10n
C303 C309 4n7
I300B I301B NE5532
I300A I301A NE5532
R306 R322 10
HI/LO CUT
SL1-B SL1-A
DUAL PARALLEL
S4-A S4-B S4-C S4-D
I600-P I300-P I301-P I302-P
C311 C312 C313 C314 0µ1
+15V -15V +UB -UB
GND LIFT
CN38 CN33
C305 4n7
S3-B

LEVEL CONTROL A
LEVEL CONTROL B
Part of 87128A
VR1 VR2 10kB
CN4X-5 CN4X-6 CN4X-7
CN4X-3CODE CN4X-4 CN4X-1 CN4X-2
CN4-5 CN4-6 CN4-7
CN4-3 CN4-4 CN4-1 CN4-2
R310 R326 3R9
R312 R328 33
C304 C310 47p
R311 R327 22k
R307 R323 6k8
I101B I201B NE5532
R313 10k
R314 10k
I302A I302B NE5532
S2-A
MONO BRIDGED
C319 C320 22µ/16V
FRNT_INA FRNT_INB
TO SHEET 2

TL072
NE5532
BC327 BC337
2N3906 2N3904

AC VOLTAGE 1000 HZ, MEASURED WITH VTVM
DC VOLTAGE MEASURED WITH VOLTMETER 100kOHM/V

DISPLAY ADJUST B
DISPLAY ADJUST A
VR600 VR601 4k7
R601 R611 6k8
D234 D233 1N4148
Q600 Q610 2N3906
Q601 Q611 BC327
R602 R612 5k6
R603 R613 2k4
R604 R614 5k6
Q602 Q612 BC337
Q603 Q613 2N3904
R605 R615 220k
D600 D601 1N4148
R606 R616 4k7
C603 C613 10µ
R607 R617 30k
I600A I600B TL072
R608 R618 5k6
DISPLAY B
DISPLAY A
FROM SHEET 2
CN2B-1 SP_B_OUT
CN2A-1 SP_A_OUT
R600 R610 20k
C600 C610 10µ

R620 470k
R621 22k
R622 100k
Q620 Q621 BC337
R623 2k4
D602 1N4148
-15.6V
MUTD
FROM SHEET 3

87128A
CN7X-3 CN7X-10 CN7X-8 CN7X-5 CN7X-9 CN7X-7 CN7X-1 CN7X-6 CN7X-4 CN7X-2
CN7-3 CN7-10 CN7-8 CN7-5 CN7-9 CN7-7 CN7-1 CN7-6 CN7-4 CN7-2
PROTECT RED D23 RED D21
LIMIT_B FROM SHEET 2 RED D19
LIMIT_A FROM SHEET 2 RED D20
IN-B GREEN D27
OUT-B GREEN D26
OUT-A GREEN D22
IN-A GREEN D24
POWER ON
R630 R631 4k7
GREEN D29
GREEN D28

ALTERATIONS RESERVED!

SYMB.	REVISION	DATE	NAME
I	151/99	6.12.99	Stangl
H	100/98	10.98	Lang
G	78/98	09.98	Lang
F	88/97	12.97	Lang
E	115/96	10.96	Lang
D	26/95	3.3.95	Brandl
C	156/94	8.12.94	Taffner
B	103/94	13.9.94	Brandl
A	Pilot	9.6.94	Brandl

Last modified: 06.12.1999 12:12:54
Last plotted: 07.05.2001 16:13:21

	DATE	NAME
DSG'D		J.Taffner
CHK'D	3.3.95	Brandl M.
APP'D		

DYNACORD

86207I MAIN_PCB
CIRCUIT DIAGRAM 1/3
348681 2-
L2400

SELECT.	INP.SENS. A
J11	0 dBu
J12	+6 dBu
	GAIN
J13	26 dB

SELECT.	INP.SENS. B
J21	0 dBu
J22	+6 dBu
	GAIN
J23	26 dB

ALTERATIONS RESERVED!

SYMB.	REVISION	DATE	NAME
			Last modified: 06.12.1999 12:12:54
I	151/99	6.12.99	Stangl
H	100/98	10.98	Lang
G	78/98	09.98	Lang
F	88/97	12.97	Lang
E	115/96	10.96	Lang
D	26/95	3.3.95	Brandl
C	156/94	8.12.94	Taffner
B	103/94	13.9.94	Brandl
A	Pilot	9.6.94	Brandl
			Last plotted: 07.05.2001 16:13:41
		DATE	NAME
DSG'D			J.Taffner
CHK'D	3.3.95		Brandl M.
APP'D			

DYNACORD

86207I MAIN PCB

CIRCUIT DIAGRAM 2/3

348682 2-

L2400

SPEED 1: 12.5V
SPEED 2: 19.5V
SPEED 3: 27.5V

SPEED 1: 3.5V
SPEED 2: 5.8V
SPEED 3: 8.5V

FAN SPEED ADJUST

TIME_IN

MUTD
TO SHEET 1 AND 2

*1: used only with PCB 86207
*2: used only with PCB 86211

AC VOLTAGE 1000 HZ, MEASURED WITH VTVM
DC VOLTAGE MEASURED WITH VOLTMETER 100kOHM/V

ALTERATIONS RESERVED!

SYMB.	REVISION	DATE	NAME
I	151/99	6.12.99	Stangl
H	100/98	10.98	Lang
G	78/98	09.98	Lang
F	88/97	12.97	Lang
E	115/96	10.96	Lang
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C	156/94	8.12.94	Taffner
B	103/94	13.9.94	Brandl
A	Pilot	9.6.94	Brandl

Last modified: 06.12.1999 12:12:54
Last plotted: 07.05.2001 16:13:55

	DATE	NAME
DSG'D		J.Taffner
CHK'D	3.3.95	Brandl M.
APP'D		

DYNACORD

86207I MAIN PCB

CIRCUIT DIAGRAM 3/3

348777

L2400

2-