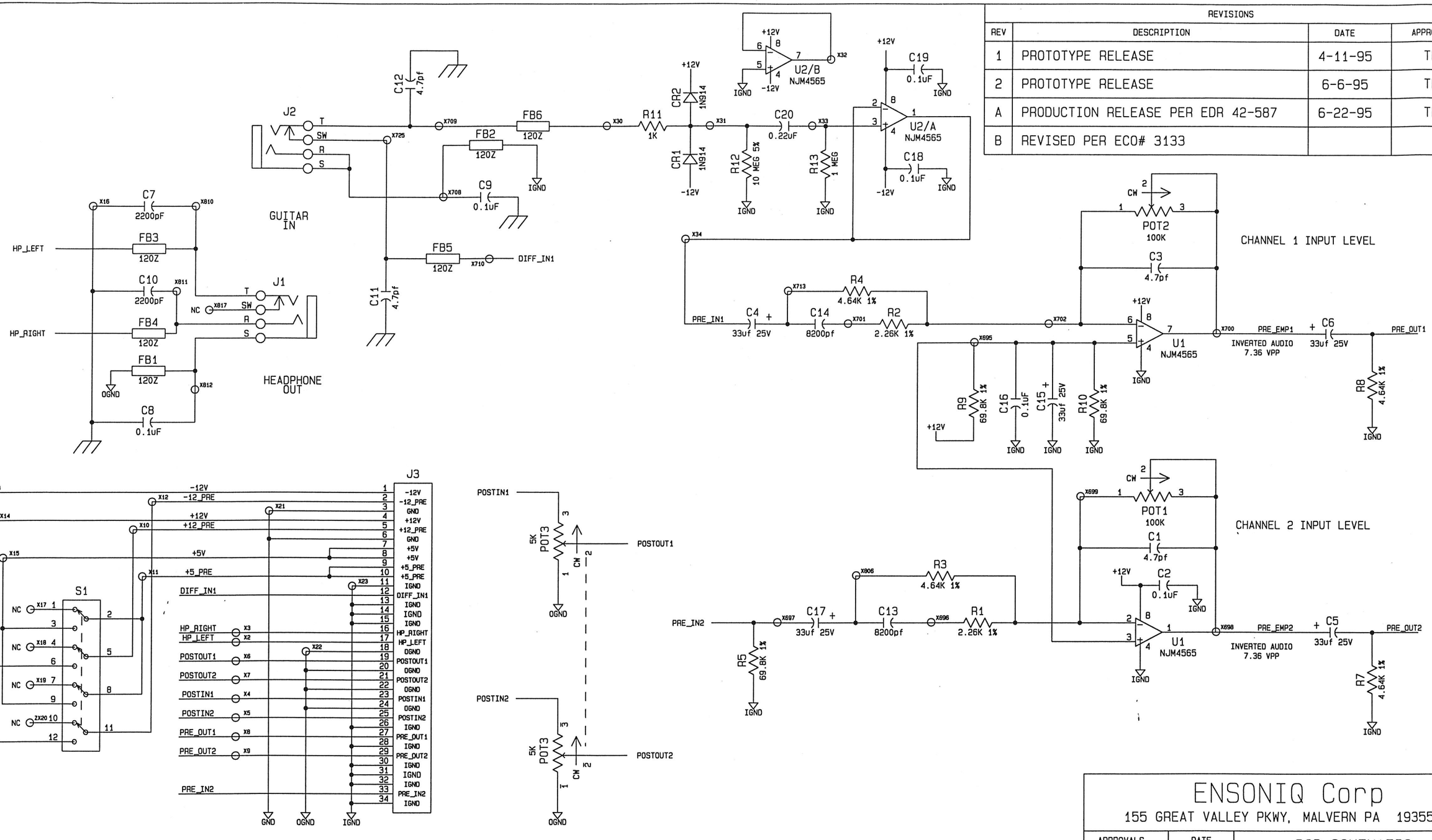
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	PROTOTYPE RELEASE	4-11-95	TM
2	PROTOTYPE RELEASE	6-6-95	TM
A	PRODUCTION RELEASE PER EDR 42-587	6-22-95	TM
B	REVISED PER ECO# 3133		



3. ALL 0.1UF BYPASS CAPACITORS ARE 50V +80/-20% Z5U.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS, 16V MINIMUM.
1. ALL RESISTOR VALUES ARE IN OHMS, 1/8W 5%.

NOTES - UNLESS SPECIFIED OTHERWISE:

ENSONIQ Corp
155 GREAT VALLEY PKWY, MALVERN PA 19355

APPROVALS	DATE	PCB SCHEMATIC FRONT PANEL JACK BOARD DP-2		
DWN BY: B.CARR	2-24-95			
CHKD BY:				
APPD BY:		SIZE B	DWG. NO. 4010028101	REV B
USED ON: XXX		SCALE —	SHEET 1 OF 1	